



1374.43323X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Akio NISHIDA, et al.  
Serial No.: 10/781,861  
Filed: February 20, 2004  
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A  
METHOD OF MANUFACTURING THE SAME  
Group: 2815  
Examiner: WARREN, Matthew E.  
Confirmation No.: 4775

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.97 AND 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

March 2, 2006

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find Form PTO/SB/08A listing the reference, U.S. Patent No. 6,307,217, to be considered by the Examiner. Applicants note that since the reference is a U.S. Patent, a copy of the document is not enclosed.

This Information Disclosure Statement is being submitted subsequent to a first Office Action on the merits in the above-identified application, but prior to a Final Office Action or issuance of a Notice of Allowance, or issuance of any other action closing prosecution on the merits in the above-identified application. Accordingly, the fee of \$180.00 is enclosed herewith.

In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 C.F.R. 1.97 and 1.98 have been satisfied, in connection with the present submission of the enclosed documents. Accordingly, consideration of the



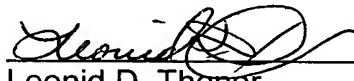
Docket No. 1374.43323X00  
Serial No. 10/781,861  
March 2, 2006

enclosed documents, during examination of the above-identified application, is respectfully requested.

Applicants request any shortage or excess in fees in connection with the filing of this paper, including extension of time fees, and for which no other form of payment is offered, be charged or credited to Deposit Account No. 01-2135 Case: 1374.43323X00).

Respectfully submitted,

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